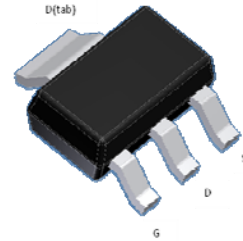
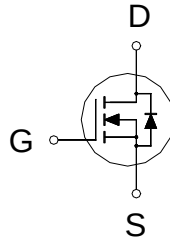


N-Channel Logic Level Enhancement Mode Field Effect Transistor

Product Summary:

BV _{DSS}	20V
R _{DS(on)} (MAX.)	24mΩ
I _D	6A



Pb-Free Lead Plating & Halogen Free



ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNIT
Gate-Source Voltage		V _{GS}	±12	V
Continuous Drain Current	T _C = 25 °C	I _D	6	A
	T _C = 100 °C		4	
Pulsed Drain Current ¹		I _{DM}	24	
Power Dissipation	T _C = 25 °C	P _D	6.25	W
	T _C = 100 °C		2.5	
Operating Junction & Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNIT
Junction-to-Case	R _{θJC}		20	°C / W
Junction-to-Ambient	R _{θJA}		100	

¹Pulse width limited by maximum junction temperature.

²Duty cycle ≤ 1%

ELECTRICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, Unless Otherwise Noted)

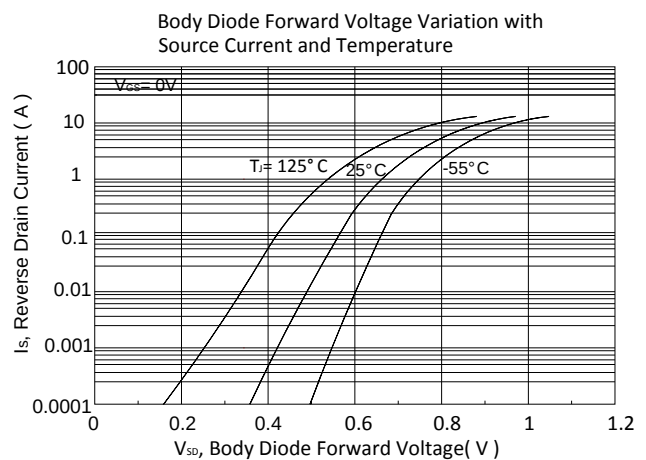
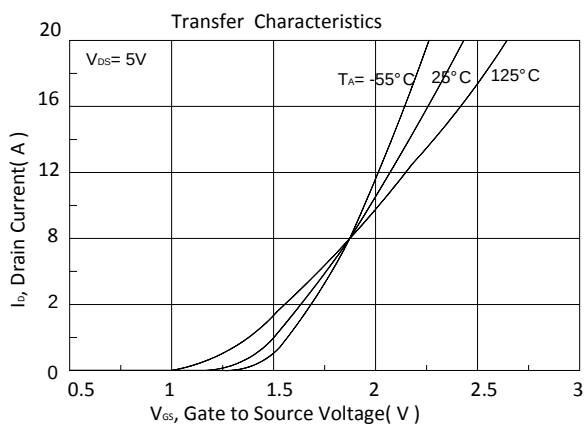
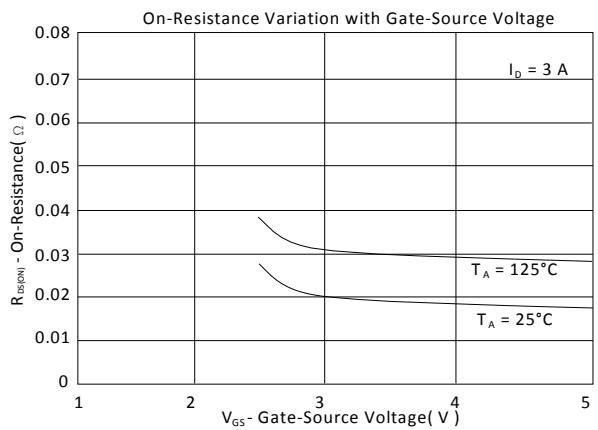
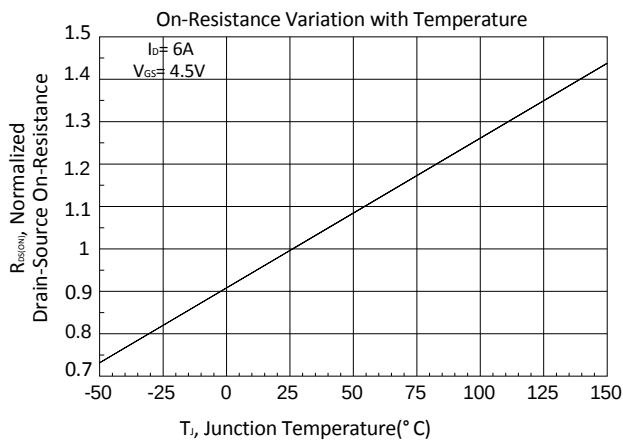
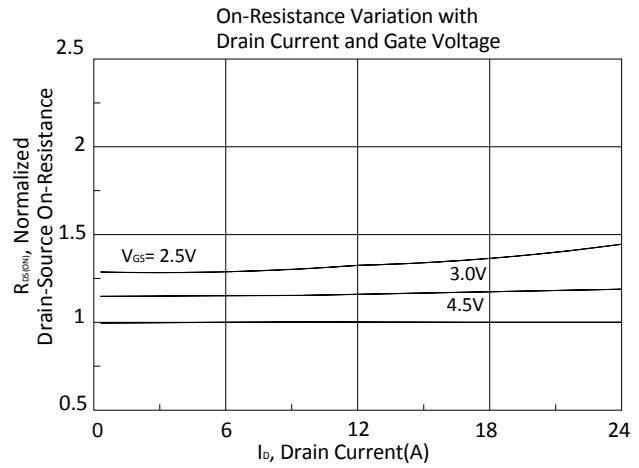
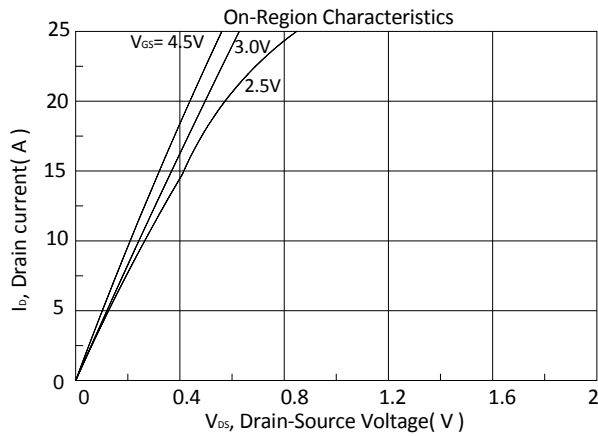
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	20			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.45	0.75	1.2	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 12V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 16V, V_{GS} = 0V$			1	μA
		$V_{DS} = 16V, V_{GS} = 0V, T_J = 125\text{ }^{\circ}C$			10	
On-State Drain Current ¹	$I_{D(ON)}$	$V_{DS} = 5V, V_{GS} = 4.5V$	6			A
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 6A$		21	24	m Ω
		$V_{GS} = 2.5V, I_D = 5A$		32	40	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 5V, I_D = 6A$		7		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 10V, f = 1MHz$		560		pF
Output Capacitance	C_{oss}			166		
Reverse Transfer Capacitance	C_{rss}			150		
Total Gate Charge ^{1,2}	Q_g	$V_{DS} = 10V, V_{GS} = 4.5V,$ $I_D = 6A$		8.5		nC
Gate-Source Charge ^{1,2}	Q_{gs}			1.5		
Gate-Drain Charge ^{1,2}	Q_{gd}			3.5		
Turn-On Delay Time ^{1,2}	$t_{d(on)}$	$V_{DS} = 10V,$ $I_D = 1A, V_{GS} = 4.5V, R_{GS} = 6\Omega$		12		nS
Rise Time ^{1,2}	t_r			15		
Turn-Off Delay Time ^{1,2}	$t_{d(off)}$			30		
Fall Time ^{1,2}	t_f			15		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_c = 25\text{ }^{\circ}C$)						
Continuous Current	I_S				2	A
Pulsed Current ³	I_{SM}				8	
Forward Voltage ¹	V_{SD}	$I_F = I_S, V_{GS} = 0V$			1.2	V

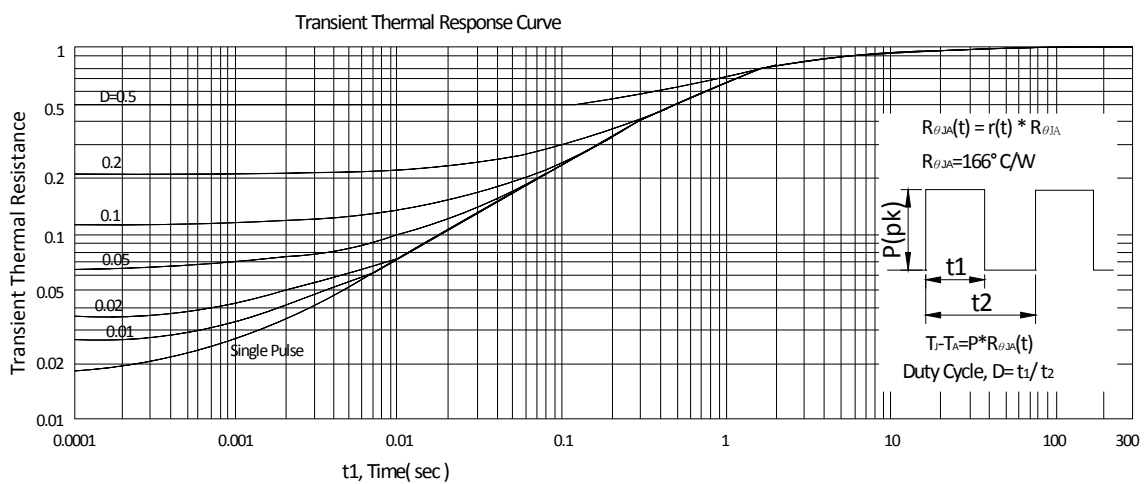
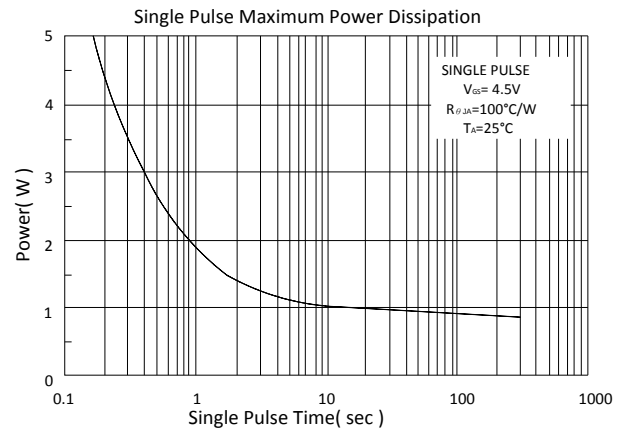
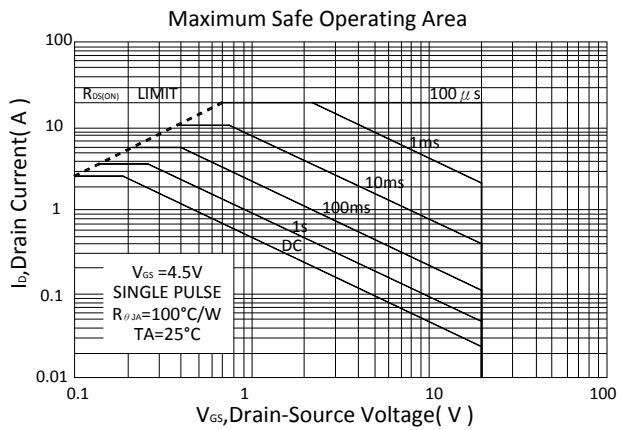
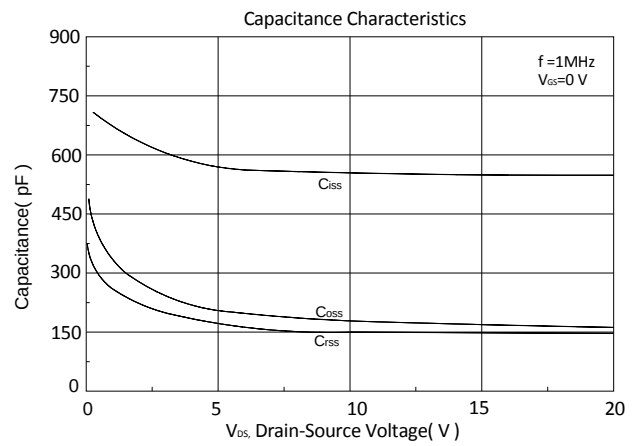
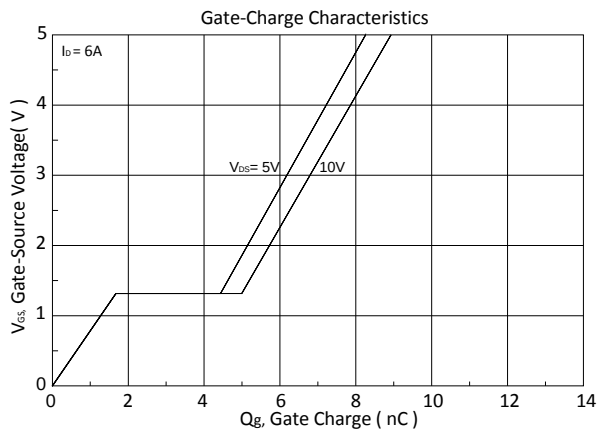
¹Pulse test : Pulse Width $\leq 300\text{ }\mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

³Pulse width limited by maximum junction temperature.

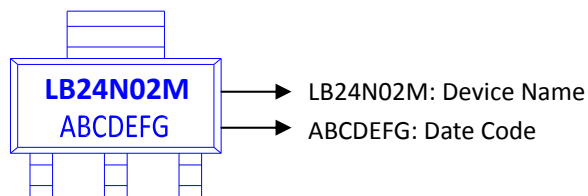
TYPICAL CHARACTERISTICS



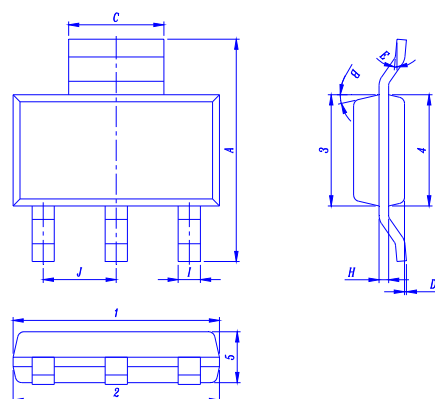


Ordering & Marking Information:

Device Name: LB24N02M for SOT-223



Outline Drawing



Dimension in mm

Dimension	A	C	D	E	I	H	B	J	1	2	3	4	5
Min.	6.70	2.90	0.02	0°	0.60	0.25			6.30	63.0	3.30	3.30	1.40
Typ.							13°	2.30					
Max.	7.30	3.10	0.10	10°	0.80	0.35			6.70	6.70	3.70	3.70	1.80

Footprint

